B.Sc., ELECTRONICS

SYLLABUS

FROM THE ACADEMIC YEAR 2023-2024 ONWARDS

MANONMANIAM SUNDARANAR UNIVERSITY, TIRUNELVELI

(As per TANSCHE common syllabus template)

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The learning outcomes-based approach implies that the outcomes are identified and considered according to the ground-work of plans. Course contents, learning activities and assessment types are designed to be consistent with the achievement of desired learning outcomes. The learning outcomes are in terms of knowledge, professional attitude, work ethics, critical thinking, self-managed learning, and adaptability, problem solving skills, communication skills, interpersonal skills and group works. At the end of a particular course/program, assessment is carried out to determine whether the desired outcomes are being achieved. This outcome assessment provides feedback to ensure that element in the teaching and learning environment are acting in concert to facilitate the nurturing of the desired outcomes. The expected learning outcomes are used as reference points that would help formulate graduate attributes, qualification descriptors, programme learning outcomes and course learning outcomes which in turn help not only in curriculum planning and development, but also in delivery and review of academic programmes.

The overall objectives of the learning outcomes-based on the curriculum framework, these are: Help formulate graduate attributes, qualification descriptors, program learning outcomes and course learning outcomes that are expected to be demonstrated by the holders of qualification. Enable prospective students, parents, employers and others to understand the nature and level of learning outcomes or attributes a graduate of a programme should be capable of demonstrating on successful completion of the programme of study. Maintain national standards and international comparability of learning outcomes and academic standards to ensure global competitiveness, and to facilitate student/graduate mobility. Provide higher education institutions an important point of reference for designing teaching-learning strategies, assessing student learning level, and periodic review of programme and academic research.

The emerging trends in electronics is a program that needs to develop a specialized skill set among the graduates to cater the need for industries. In recent years, electronic science has made unprecedented growth in terms of new technologies, new ideas and principles. The research organizations and industries that work in this frontier area are in need of highly skilled and scientifically oriented manpower. This manpower can be available only with flexible, adaptive and progressive training programs and a cohesive interaction among the research organizations, academicians and industries. The key areas of study within the subject area of electronic science comprises of: Semiconductor devices, Analog and digital circuit design, Microprocessors & microcontrollers, Communication techniques, IoTs for Electronics, artificial intelligence, embedded systems, machine learning, computer hardware's, computer coding/programming skills in high/low level languages, etc.

Programme:	B.Sc. ELECTRONICS
Programme Code:	
Duration:	3 Years (UG)
Programme Outcomes:	
Programme Outcomes:	PO1: Disciplinary knowledge: A comprehensive knowledge and understanding phenomena of one or more disciplines that form a part of an undergraduate programme of study PO2: Communication Skills: Ability to express thoughts and ideas effectively in writing and orally; Communicate with others confidently share one's views and express herself/himself; demonstrate the ability to listen carefully, read and write analytically and present complex information in a clear and concise manner to different groups. PO3: Critical thinking: Capability to apply analytic thought to a body of knowledge; analyse and evaluate evidence, arguments, claims, beliefs on the basis of empirical evidence; identify relevant assumptions or implications; formulate coherent arguments; critically evaluate practices policies and theories by following scientific approach to knowledge development. PO4: Problem solving: Capacity to extrapolate from what one has learned and apply their competencies to solve different kinds of nonfamiliar problems, rather than replicate curriculum content knowledge and apply one's learning to real life situations. PO5: Analytical reasoning: Ability to evaluate the reliability and relevance of evidence; identify logical flaws and holes in the arguments of others; analyse and synthesize data from a variety of sources; draw valid conclusions and support them with evidence and examples, and addressing opposing viewpoints. PO6: Research-related skills: A sense of inquiry and capability for asking relevant/appropriate questions, problem arising, synthesising and articulating; Ability to recognise cause-and-effect relationships, define problems, formulate hypotheses, test hypotheses, analyse, interpret and draw conclusions from data, establish hypotheses, predict cause-and-effect relationships, ability to plan, execute and report the results of a experiment or investigation PO7: Cooperation/Team work: Ability to work effectively and respectfully with diverse teams; facilitate cooperative or coordinated effort on the part of a

PO 11 Self-directed learning: Ability to work independently, identify appropriate resources required for a project, and manage a project through to completion.

PO 12 Multicultural competence: Possess knowledge of the values and beliefs of multiple cultures and a global perspective; and capability to effectively engage in a multicultural society and interact respectfully with diverse groups.

PO 13: Moral and ethical awareness/reasoning: Ability to embrace formulate moral/ethical values in conducting one's life. position/argument about an ethical issue from multiple perspectives, and use ethical practices in all work. Capable of demonstrating the ability to identify ethical issues related to ones work, avoid unethical behaviour such as fabrication, falsification or misrepresentation of data or committing plagiarism, not adhering to intellectual property rights; appreciating environmental and sustainability issues; and adopting objective, unbiased and truthful actions in all aspects of work.

PO 14: Leadership readiness/qualities: Capability for mapping out the tasks of a team or an organization, and setting direction, formulating an inspiring vision, building a team who can help achieve the vision, motivating and inspiring team members to engage with that vision, and using management skills to guide people to the right destination, in a smooth and efficient way.

PO 15: Lifelong learning: Ability to acquire knowledge and skills, how to learn, that are necessary for participating in learning activities throughout life, through self-paced and self-directed learning aimed at personal development, meeting economic, social and cultural objectives, and adapting to changing trades and demands of work place through knowledge/skill development/reskilling.

Programme **Outcomes:**

Specific On successful completion of Bachelor of Science (B.Sc) in Electronics programme, the student should be able to:

> **PSO1:** Disciplinary Knowledge: Understand the fundamental principles, concepts and theories related to electronics science. Also, exhibit proficiency in performing experiments in the laboratory.

> **PSO2:** Critical Thinking: Analyse complex problems, evaluate information, synthesize information, apply theoretical concepts to practical situations, identify assumptions and biases, make informed decisions and communicate effectively

> **PSO3: Problem Solving:** Employ theoretical concepts and critical reasoning ability with physical, mathematical and technical skills to solve problems, acquire data, analyse their physical significance and explore new design possibilities.

> **PSO4:** Analytical & Scientific Reasoning: Apply scientific methods, collect and analyse data, test hypotheses, evaluate evidence, apply

statistical techniques and use computational models.

PSO5: Research related skills: Formulate research questions, conduct literature reviews, design and execute research studies, communicate research findings and collaborate in research projects.

PSO6: Self-directed &Lifelong Learning: Set learning goals, manage their own learning, reflect on their learning, adapt to new contexts, seek out new knowledge, collaborate with others and to continuously improve their skills and knowledge, through ongoing learning and professional development, and contribute to the growth and development of their field.

PO/PSO	PSO1	PSO2	PSO3	PSO4	PSO5	PSO6
PO1	✓					
PO2		✓				
PO3			✓			
PO4				✓		
PO5					✓	
PO6						✓

2. Highlights of the revamped Curriculum:

- ➤ Student-centric, meeting the demands of industry & society, incorporating industrial components, hands-on training, skill enhancement modules, lab and project with viva-voce examinations, exposure to entrepreneurial skills, training for competitive examinations, sustaining the quality of the core components and incorporating application-oriented content wherever required.
- The core subjects include latest developments in the education and scientific front, advanced programming packages allied with the discipline topics, practical training, devising statistical models and algorithms for providing solutions to industry orreal-life situations. The curriculum also facilitates peer learning with advanced statistical topics in the final semester, catering to the needs of stakeholders with aptitude.
- The general term, "problem solving" skills are included as mandatory components in the 'Training for competitive examinations' course at the final semester.
- The curriculum is designed so as to strengthen the industry-academia interface and provide more job opportunities for the students.
- ➤ The Internship during the second-year vacation will help the students gain valuable work experience that connects classroom knowledge to real world experience and to narrow down and focus on the career path.

- A practical and project with viva-voce components are enabling the student with application of conceptual knowledge to practical situations. The state of art technologies in conducting a scientific and systematic way is ensured. Such innovative provisions of the industrial training, project and internships will give students an edge over the counterparts in the job market.
- > State-of Art techniques from the streams of multi-disciplinary, cross disciplinary and inter disciplinary nature are incorporated as elective courses, covering conventional to the latest topics.

Valueadditionsinthe revamped curriculum:

Semester	NewlyintroducedComponents	Outcome/ Benefits
I	FoundationCourse To ease the transition of learningfrom higher secondary to highereducation, providing an over view of the pedagogy of learning literature and analyzing the world through the literary to an ewperspective.	 Impart confidenceamongthe students Createinterestforthesubject
I, II, III, IV	SkillEnhancementpapers(Discipline centric/Generic/Entrepreneurial)	 Industryreadygraduates Skilledhumanresource Studentsareequippedwithessentialskill sto makethememployable Trainingonlanguageandcommunicatio nskillsenablethe student's gain knowledgeandexposureinthecompetiti veworld. Discipline centric skillwilling provethe technicalknowhowofsolvingreallifeproblems.
III, IV, V& VI	Elective papers	 Strengthening thedomainknowledge Introducing thestakeholders to theState-of Arttechniquesfrom the streamsofmulti- disciplinary,crossdisciplinaryandinter disciplinarynature Emerging topics inhigher education/industry/communicationnet work/healthsectoretc.areintroducedwit hHands-ontraining.

IV	Elective papers	 Exposuretoindustrymoldsstudentsinto solutionproviders GeneratesIndustryreadygraduates Employmentopportunitiesenhanced
V	Elective papers	 Self-learning isenhanced Applicationoftheconcepttorealsituation isconceivedresultingIntangibleoutcome
VI	Electivepapers	 Enriches the studybeyondthe course. Developingaresearchframework andpresenting themindependent andIntellectualideaseffectively.
Extra Credits:	_	> Tocatertotheneedsofpeerlearners/resea
ForAdvancedLearners/Ho	norsdegree	rch aspirants
SkillsacquiredfromtheCou	ability	ledge, Problem solving, Analytical ,Professionalcompetency,Professionalcommunica dTransferrableskill

Credit Distribution for UG Programmes

Sem I	Credit	Н	Sem II	Credit	Н	Sem III	Credit	Н	Sem IV	Credit	Н	Sem V	Credit	H	Sem VI	Credit	H
Part 1. Language – Tamil	3	6	Part.1. Language – Tamil	3	6	Part.1. Language – Tamil	3	6	Part.1. Language – Tamil	3	6	5.1 Core Course – \CC IX	4	5	6.1 Core Course – CC XIII	4	6
Part.2 English	3	6	Part.2 English	3	6	Part.2 English	3	6	Part.2 English	3	6	5.2 Core Course – CC X	4	5	6.2 Core Course – CC XIV	4	6
1.3 Core Course – CC I	4	5	23 Core Course – CC III	4	5	3.3 Core Course - CC V	4	5	4.3 Core Course – CC VII Core Industry Module	4	5	5. 3. Core Course CC -XI	4	5	6.3 Core Course – CC XV	4	6
1.4 Core Course – CC II+Allied	3+3	3+2	2.4 Core Course – CC IV+ Allied	3+3	3+2	3.4 Core Course – CC VI+ Allied	3+3	3+2	4.4 Core Course – CC VIII+ Allied	3+3	3+2	5. 4. Core Course –/ Project with viva- voce CC -XII	4	5	6.4 Elective -VII Generic/ Discipline Specific	3	5
1.5 Elective I Generic/ Discipline Specific	3	4	2.5 Elective II Generic/ Discipline Specific	3	4	3.5 Elective III Generic/ Discipline Specific	3	4	4.5 Elective IV Generic/ Discipline Specific	3	3	5.5 Elective V Generic/ Discipline Specific	3	4	6.5 Elective VIII Generic/ Discipline Specific	3	5
1.6 Skill Enhancement Course SEC-1	2	2	2.6 Skill Enhancement Course SEC-2	2	2	3.6 Skill Enhancement Course SEC-4, (Entrepreneurial Skill)	1	1	4.6 Skill Enhancement Course SEC-6	2	2	5.6 Elective VI Generic/ Discipline Specific	3	4	6.6 Extension Activity	1	-
1.7 Skill Enhancement -(Foundation Course)	2	2	2.7 Skill Enhancement Course – SEC-3	2	2	3.7 Skill Enhancement Course SEC-5	2	2	4.7 Skill Enhancement Course SEC- 7	2	2	5.7 Value Education	2	2	6.7 Professional Competency Skill	2	2
						3.8 E.V.S.	-	1	4.8 E.V. S	2	1	5.8 Summer Internship /Industrial Training	2				
	23	30		23	30		22	30		25	30		26	30		21	30

Total - 140 Credits

Choice Based Credit System (CBCS), Learning Outcomes Based Curriculum Framework (LOCF) Guideline Based Credit and Hours Distribution System for all UG courses including Lab Hours

First Year - Semester-I

Part	List of Courses	Credit	No. of
			Hours
Part-1	Language – Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses& Elective Courses [in Total]	13	14
	Skill Enhancement Course SEC-1	2	2
Part-4	Foundation Course	2	2
		23	30

Semester-II

Part	List of Courses	Credit	No. of
			Hours
Part-1	Language – Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses& Elective Courses including laboratory [in Total]	13	14
Part-4	Skill Enhancement Course -SEC-2	2	2
	Skill Enhancement Course -SEC-3 (Discipline / Subject Specific)	2	2
		23	30

Second Year - Semester-III

Part	List of Courses	Credit	No. of Hours
Part-1	Language – Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses& Elective Courses including laboratory [in Total]	13	14
Part-4	Skill Enhancement Course -SEC-4 (Entrepreneurial Based)	1	1
	Skill Enhancement Course -SEC-5 (Discipline / Subject Specific)	2	2
	E.V.S	-	1
		22	30

Semester-IV

Part	List of Courses	Credit	No. of Hours
Part-1	Language – Tamil	3	6
Part-2	English	3	6
Part-3	Core Courses& Elective Courses including laboratory [in Total]	13	13
Part-4	Skill Enhancement Course -SEC-6 (Discipline / Subject Specific)	2	2

Skill Enhancement Course -SEC-7	(Discipline / Subject Specific)	2	2
E.V.S		2	1
		25	30

Third Year - Semester-V

Part	List of Courses	Credit	No. of Hours
Part-3	Core Courses including Project / Elective Based	22	26
Part-4	Value Education	2	2
	Internship / Industrial Visit / Field Visit	2	2
		26	30

Semester-VI

Part	List of Courses	Credit	No. of
			Hours
Part-3	Core Courses including Project / Elective Based & LAB	18	28
Part-4	Extension Activity	1	-
	Professional Competency Skill	2	2
		21	30

Consolidated Semester wise and Component wise Credit distribution

Parts	Sem I	Sem II	Sem III	Sem IV	Sem V	Sem VI	Total Credits
Part I	3	3	3	3	-	-	12
Part II	3	3	3	3	-	-	12
Part III	13	13	13	13	22	18	92
Part IV	4	4	3	6	4	1	22
Part V	-	-	-	-	-	2	2
Total	23	23	22	25	26	21	140

*Part I. II, and Part III components will be separately taken into account for CGPA calculation and classification for the under graduate programme and the other components. IV, V have to be completed during the duration of the programme as per the norms, to be eligible for obtaining the UG degree.

MethodsofEvaluation- Theory								
	ContinuousInternalAssessmentTest							
Internal	Assignments	25 Marks						
Evaluation	Seminars							
	AttendanceandClassParticipation							
External	EndSemesterExamination	75 Marks						
Evaluation	EndSemesterExamination	/3 Marks						
	Total 100 Marks							
	Methods of Evaluation-Practical							
Internal Evaluation								
	maintenance, model practical examination							
External Evaluation	EndSemesterExamination with viva-voce	50 Marks						
	MethodsofAssessment							
Recall(K1)	Simpledefinitions, MCQ, Recallsteps, Concept definitions							
Understand/C	MCQ,True/False,Shortessays,Conceptexplanations, short	summaryor						
omprehend (K2)	Overview							
Application (I/2)	Suggestidea/conceptwithexamples, suggest formulae, Solvenia	veproblems,						
Application (K3)	Observe, Explain							
Analyze(K4)	Problem-solvingquestions, finish aprocedureinmanysteps,Differentiate							
	Betweenvariousideas, Mapknowledge							
Evaluate(K5)	Longer essay/Evaluationessay, Critiqueorjustify with prosand cons							
Cwanta(VC)	Checkknowledgeinspecificoroffbeatsituations, Discussion,							
Create(K6)	Presentations							

FIRST SEMESTER

Sl.No.	Course	Course	Cr	edit	distr	ibution	Overall	Total	Mark	S	
	Category					Credits	contact				
								Hours/week	CIA	ESE	Total
			L	T	P	S					
1	Part –I	Language- Tamil	L				3	6	25	75	100
2	Part –II	English	L				3	6	25	75	100
3	Part -III	CC-1	L				4	5	25	75	100
4	Part -III	CC-2			P		3	3	50	50	100
5	Part -III	AL-1	L				3	2	25	75	100
6	Part -III	Elective I Generic /	L				3	4	25	75	100
		Discipline Specific									
7	Part –IV	SEC-1 (NME)	L				2	2	25	75	100
8	Part –IV	SE-FC	L				2	2	25	75	100
		Total					23	30			

SECOND SEMESTER

Sl.No.	Course	Course			Credi		Overall	Total		Marks		
	Category			dis	tribut	ion	Credits	contact				
								Hours/week	CIA	ESE	Total	
			L	T	P	S						
1	Part –I	Language-	L				3	6	25	75	100	
		Tamil										
2	Part –II	English	L				3	6	25	75	100	
3	Part -III	CC-3	L				4	5	25	75	100	
4	Part -III	CC-4			P		3	3	50	50	100	
5	Part -III	AL-2	L				3	2	25	75	100	
6	Part -III	Elective II	L				3	4	25	75	100	
		Generic /										
		Discipline										
		Specific										
7	Part –IV	SEC-2	L				2	2	25	75	100	
		(NME)										
8	Part –IV	SEC-3	L		_		2	2	25	75	100	
		Total					23	30				

THIRD SEMESTER

Sl.No	Course	Course	Cre	edit			Overall	Total contact	Marks		
	Category		dis	tribu	tion		Credits	Hours/week			
			L	T	P	S			CIA	ESE	Total
1	Part –I	Language-	L				3	6	25	75	100
		Tamil									
2	Part –II	English	L				3	6	25	75	100
3	Part –III	CC-5	L				4	5	25	75	100
4	Part –III	CC-6			P		3	3	50	50	100
5	Part –III	AL-3	L				3	2	25	75	100
6	Part –III	Elective III Generic /	L				3	4	25	75	100
		Discipline Specific									
7	Part –IV	SEC-4	L				1	1	25	75	100
8	Part –IV	SEC-5	L				2	2	25	75	100
9	Part –IV	E.V. S	L				-	1	25	75	100
	Total						22	30			

FOURTH SEMESTER

Sl.NO	Course	Course	Course	Cre	edit			Overall	Total contact		Marks	
	Category	Code		dis	distribution			Credits	Hours/week			
				L	T	P	S			CIA	ESE	Total
1	Part –I		Language-	L				3	6	25	75	100
			Tamil									
2	Part –II		English	L				3	6	25	75	100
3	Part –III		CC VII	L				4	5	25	75	100
4	Part –III		CC VIII			P		3	3	50	50	100
5	Part –III		AL IV	L				3	2	25	75	100
6	Part –III		Elective IV	L				3	3	25	75	100
			Generic /									
			Discipline									
			Specific									
7	Part –IV		SEC-6	L				2	2	25	75	100
8	Part –IV		SEC-7	L				2	2	25	75	100
9	Part –IV		EVS	L				2	1	25	75	100
		Total	1			•		25	30		•	

FIFTH SEMESTER

Sl. NO	Course Category	Course	Credit distribution		stribu	ition	Overall Credits	Total contact Hours/week	Marks			
			L	T	P	S			CIA	ESE	Total	
1	Part -III	CC- IX	L				4	5	25	75	100	
2	Part –III	CC –X	L				4	5	25	75	100	
3	Part -III	CC- XI			P		4	5	50	50	100	
4	Part -III	Core course/ Project with viva- voce- XII					4	5	25	75	100	
5	Part -III	Elective-5	L				3	4	25	75	100	
6	Part -III	Elective-6	L				3	4	25	75	100	
7	Part -IV	Value Education					2	2	25	75	100	
8	Part -IV	Internship/ Industrial visit/ Field visit					2	-	25	75	100	
	Total						26	30				

SIXTH SEMESTER

	Course Category	Course Code	Course	Cre	dit dis	tribu	tion	Overall Credits	Total contact	Marks		
	Cutogory	Code						Credits	Hours/week			
				L	T	P	S			CIA	ESE	Total
1	Part -III		CC-XIII	L				4	6	25	75	100
2	Part -III		CC-XIV	L				4	6	25	75	100
3	Part -III		CC-XV			P		4	6	50	50	100
4	Part -III		Elective-7	L				3	5	25	75	100
5	Part -III		Elective-8	L				3	5	25	75	100
6	Part -IV		Extension activity					1	-	-	-	-
7	Part -IV		Professional competency skill	L				2	2	25	75	100
		Total						21	30			

Credit Distribution for B.Sc., ELECTRONICS

S.No	Part	Course Details	Credit
1	III	Core	68
2		Elective Generic/ Discipline Specific Elective	24
3	I& II	Language & English	24
		(Lang - 4x3 = 12)	
		Eng - 4x3 = 12	
4		NME(2x2)	4
5		EVS(1x2)	2
6		Value Education(1x2)	2
7		Extension Activity(1x1)	1
8		Ability Enhancement [AECC]- Soft Skill	8
	IV& V	(4x2=8)	9
		Skill Enhancement Course [4 Courses x 2 credits]	
		=8 credits], SEC-4: 1 Credit	2
		Summer internship/ Industrial training	
		(2x1=2 credits)	2
		Foundation course	2
		Professional Competency Skill	
			140

Remarks: English Soft Skill Two Hours Will be handled by English Teachers (4+2 = 6 hours for English).

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.		Marks	
Code							dits	Hou rs	CIA	External	Total
	BASICELECTR ONICDEVICES	Core Course – 1 (CC-I)	5	0	-	-	4	5	25	75	100
		Cour	se o	bje	ctiv	es					
				•							
CO1	Learn the fundamental							evices.			
CO2	Provide the in-depth kr										
CO3	Understand the basicco	ncepts of ser	nico	ndu	ctor	s ar	nd its cl	naracter	istics		
CO4	Examines the principle SCR characteristics	xamines the principles and operations of transistors and understand the basics of UJT and CR characteristics									
CO5	Know the basics of FE	Ts and MOS	FET	cor	npo	nen	ts				
UNIT	Details No.of Course Hours Objecti										
I									CO1		
II	Atomic structure, Boh bands –classification energy gap – intrinsic	r's atom mo of solids and and extrinsic	del d en	– e erg	y ba ondu	ands ictor	s – for rs, P ty	bidden	12	CO2	
III	PN junction- Biasing biasing – PN junction resistance - diode recti Bridge rectifier – cl	N type semiconductors— majority and minority carriers PN junction— Biasing a PN junction— forward and reverse 12 CO3 biasing—PN junction diode: characteristics—static and dynamic resistance—diode rectifiers: Half wave and Full wave rectifier— Bridge rectifier— clippers and clampers—Zener diode— Characteristics-voltage regulation using Zener diode									
IV	Bipolar transistor – UJT – Common Base, Common Emitter & 12 Common Collector configurations and their characteristics – transistor biasing methods - Transistor as switch, amplifier – SCR										
V	FET Constructional f characteristics - JFET enhancement and deple	and MOSFE	_		_				12	CO5	
	Total								60		

	Course Outcomes									
Course	On completion of this course, students can able to									
Outcomes										
CO1	Study the basic semiconductor devices and their	PO5, PO6, PO10								
	characterisation.									
CO2	Gain the knowledge of detailed functions of semiconductors.	PO10								
CO3	Inderstand the various types of semiconductor devices PO11									
	behaviours, different types of semiconductors									
CO4	Explain the principles and working mechanism of different	PO4, PO11								
	types of semiconductors and the scope of application.									
CO5	Understand the concept of device functionalities andhelp the PO4, PO11									
	students to understand the basic electronic devices									
	Text Books									
1	V.K.Mehta, "Principles of electronics", S.Chand & Co.,									
2	B.L.Theraja, "Basic solid-state electronics", S.Chand & Co.,									
	References Books									
1	Semiconductor Physics and Devices-Basic Principles 4 ^{tl}	Edition. by Donald A.								
	Neamen(2021)									
	Web Resources									
1	https://www.electronics-tutorials.ws/diode/diode_1.html									
2	https://www.electronicshub.org/types-of-semiconductor-devices/									
3	https://www.britannica.com/technology/semiconductor-device									
	Methods of Evaluation									
	Continuous Internal Assessment Test									
Internal	Assignments	25 Marks								
Evaluation										
	Attendance and Class Participation									
External	End Semester Examination	75 Marks								
Evaluation										
	Total	100 Marks								
	Methods of Assessment									
Recall (K1										
Understand										
Comprehen	d MCQ, True/False, Short essays, Concept explanations, short	summary or overview								
(K2)		11 01								
Application		ve problems, Observe,								
(K3)	Explain	D:00 1:11								
Analyse (K4	Problem-solving questions, finish a procedure in many steps	, Differentiate between								
	various ideas, Map knowledge									

Evaluate (K5)	Longer essay/ Evaluation essay, Critique or justify with pros and cons							
Create (V6)	Check knowledge in specific or offbeat situations, Discussion, Debating or							
Create (K6)	Presentations							

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1					M	M				M	
CO2										M	M
CO3											S
CO4				M							S
CO5				M							S

Subject	Subject Name	Category	L	T	P	S	Cr	Inst.		Mark	S	
Code							edi	Hou	CIA	Externa	al Total	
							ts	rs				
	BASIC	Core	-	-	3	-	3	3	50	50	100	
	ELECTRONIC	Course										
	DEVICES LAB	II-										
		Practical										
		I (CC-II)										
		Co	lire	e ()	hiec	<u>tives</u>						
			uis	CO	bjec	uves						
CO1	Gain knowledge	of electronic	s co	mp	oner	nts						
CO2	Examine the curr	ent & voltag	e cl	nara	cteri	stics	of ser	nicondu	ctor de	vices		
CO3	Identifythe variou	ıs device par	ram	eter	s fro	m I-	V char	acterist	ics			
CO4	Extract important	information	ı fro	m t	he g	raphi	cal pl	ots of de	evice ch	naracterist	tics	
CO5	Interpret the expe	rimental dat	a to	un	derst	and t	the bel	haviour	of the c	levice		
UNIT		De	tail	S					No.	of (Course	
I PN Junctiondiode and Zenerdiode Characteristics						6	CO1					
II	Bipolar Junction									6	CO2	
		Output) - Common Base (CB); BJT Characteristics (Input										
	and Output) – C	ommon Emi	itter	(C)	E); I	BJT (Chara	cteristic	s			

	(Input and Output)— Common Collector (CC);		
	Measurement of stability factor of self-biasing method;		
	Measurement of stability factor of fixed-biasing method		
III	Field Effect Transistor (FET)characteristics	6	CO3
IV	Photoconductivity measurements of LDR; Photodiode	6	CO4
	characteristics; Phototransistor characteristics		
V	UJT and SCR characteristics	6	CO5
	Total	30	

	Total	30								
	Course Outcomes									
Course	On completion of this course, students will	1;								
Outcomes										
CO1	Practice with active and passive semiconductor devices	PO4, P	O7, PO8,							
		· ·	, PO11							
CO2	Learn the semiconductor device characteristics.		O7, PO8,							
		P	09							
CO3	Understand the basic semiconductor components working		O7, PO8,							
	principles and methodology used inside the laboratory	PO9	, PO11							
	environment									
CO4	Design, construct the electronic circuits and observe the		O7, PO8,							
	characteristics.		09							
CO5	Study and compare semiconductor device characterisation	PO4, P	O7, PO8,							
	P	O9								
	Text Books									
1	V.K.Mehta, "Principles of electronics", S.Chand & Co									
2	B.L.Theraja, "Basic solid-state electronics", S.Chand & Co									
	References Books									
1	Semiconductor Physics and Devices-Basic Principles, 4th Edition	n. by Dona	ld A.							
1	Neamen(2021)									
	Web Resources									
1	https://www.electronics-tutorials.ws/diode/diode_1.html									
2	https://www.electronicshub.org/types-of-semiconductor-device	es/								
3	https://www.britannica.com/technology/semiconductor-device									
	Methods of Evaluation									
Internal	Internal Assignments									
Evaluation	Seminars	25 Marks								
	Attendance and Class Participation									
External	End Semester Examination	75 Marks								

Evaluation										
	Total	100 Marks								
Methods of Assessment										
Recall (K1)	Recall (K1) Simple definitions, MCQ, Recall steps, Concept definitions									
Understand/	MCQ, True/False, Short essays, Concept explanations	chart cummary or								
Comprehend	overview	, short summary or								
(K2)	OVELVIEW									
Application	Suggest idea/concept with examples, suggest formul	ae, solve problems,								
(K3)	Observe, Explain									
Analyze (K4)	Problem-solving questions, finish a procedure in many	steps, Differentiate								
Allalyze (K4)	between various ideas, Map knowledge									
Evaluate	Longer essay/ Evaluation essay, Critique or justify with pr	eas and aons								
(K5)	Longer essay/ Evaluation essay, entitique of justify with pr	us and cons								
Create (K6)	Check knowledge in specific or offbeat situations, Dis-	cussion, Debating or								
Create (IXO)	Presentations	esentations								

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1				M			L	M	L		M
CO2				S			L	L	L		
CO3				S			M	M	L		M
CO4				S			M	L	L		
CO5				S			M	L	L		

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.		Marks	
Code							dits	Hou rs	CIA	External	Total
	INTRODUCTION OF C LANGUAGE	Allied Course – 1 (AL-I)	0	3	-	-	3	2	25	75	100
		Cour	se o	bje	ctiv	es					
CO1	Understand the basic C	programmin	ıg la	ngu	age						
CO2	Learn data structures a	Learn data structures and concepts									
CO3	Know the statements of C-program										
CO4	Understand functions,	Understand functions, pointers and structures									

CO5	Use arrays, pointers and strings to the C-program							
UNIT	Details	No.of Hours	Course Objectives					
Ι	INTRODUCTION: Concept of Programming Languages - High Level, Low Level, Assembly Language - Concept of Algorithms and Flow Charts - Language translators: Assemblers, Compilers, Interpreters (Only concept and differences)	4	CO1					
II	DATA CONCEPTS : Overview of C, Features of C fundamentals - Character Set, Identifiers, Keywords, Data Types, Constants, Variables, Operators - Arithmetic, Logical, Relational, Unary, Assignment, Conditional and Bitwise Operators - expressions	4	CO2					
III	STATEMENTS: Structure of C Program - Library Functions - Data input and output, Compilation and Execution of C programs - Control Statements - IF Statement, IFELSE Statement, Nesting of IFElseStatement - Operator - Switch Statement - Loop Controls - FOR, WHILE, DO-WHILE Loops, Break - Continue, Exit, GOTO Statement.	4	CO3					
IV	FUNCTIONS: The Need of a Function - definition - User Defined and Library Function - Prototype of a Function - Calling of a function - Function Argument - Passing arguments to function - Return Values -Nesting of Function - main () - Command Line Argument - Recursion.	4	CO4					
V	ARRAYS AND STRINGS: Arrays -Single and Multi- dimensional arrays, Declaration and Initialization of arrays and strings, pointers and one-dimensional arrays-Structures- Definition, declaration of structurevariables, accessing structure members unions-Data files-opening and closing a data file, creating a datafile.	4	CO5					
	Total	20						
	Course Outcomes							
Course Outcomes	On completion of this course, students can able to:							
CO1	Study the concept of basic C-programming language.		06, PO10					
CO2	Gain the knowledge of data types.	PO10						
CO3	CO3 Understand the various types of statements PO11							
CO4	, , , , , , , , , , , , , , , , , , ,							
CO5 Understand the Arrays and Strings of C-program PO4, PO11								
	Text Books							
1	E. Balaguruswami, Programming with C, TMH.							

2	Byron Gottfried, Programming with C, Schaum's Outline Series, T	MH.								
-	References Books									
1	Mahapatra, Thinking in C, PHI.									
2	Brain W Kernighan and Dennis M Ritchie, The C Programming language, PHI									
3	Dennis & Ritchie: "Programming in C".									
	Web Resources									
1	www.cprogramming.com									
2	https://archive.nptel.ac.in/courses									
3	www.programmersheaven.com									
	Methods of Evaluation									
	Continuous Internal Assessment Test									
Internal	Assignments	25 Marks								
Evaluation	Seminars	23 Warks								
	Attendance and Class Participation									
External Evaluation	End Semester Examination	75 Marks								
	Total	100 Marks								
	Methods of Assessment									
Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definitions									
Understand Comprehen		mmary or overview								
(K2)	Suggest idea/concent with examples suggest formulae solve	nrohlame Obsarvo								
Application Suggest idea/concept with examples, suggest formulae, solve problems, Observe, Explain										
Analyse (K4) Problem-solving questions, finish a procedure in many steps, Differentiate between various ideas, Map knowledge										
Evaluate (K	Longer essay/ Evaluation essay, Critique or justify with pros an	d cons								
Create (K6)	Check knowledge in specific or offbeat situations, Discu Presentations	ssion, Debating or								

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1					M	M				M	
CO2										M	M
CO3											S
CO4				M							S
CO5				M							S

Subject	Sul	bject Name	Category	L	T	P	S	Credits	Inst.	Mark	S						
Code									Hours	CIA	Exte rnal	Total					
	IN	TRODUCTION	Elective	4	-	-	-	3	4	25	75	100					
		OF ROBOTICS	Generic /														
		AND	Discipline														
	A	AUTOMATION	Specific														
			Elective-														
		I(DSE-I)															
			Cours	se (bj	ec	tiv	es		ı		1					
CO1		Attain the knowled	ge ofrobotics	an	d a	uto	m	ation.									
CO2		Study the sensors/	driver's activ	ity,	art	tifi	cia	l intellige:	nce								
CO3		Identify the robotic	s control and	au	ton	nat	ior	1									
CO4		Describe the CNC	machine feat	ures	S												
CO5		Understand sensors	for robots, r	nac	hin	ie c	on	trol and lo	ogical con	trol ass	emblies	5					
UNIT	•		Details							No.	of	Course					
										Hou	rs O	bjectives					
I		INTRODUCTION: Introduction of Robotics and 12 CO1									CO1						
		programmable au															
		robotics, robot def															
II		systems androbotic ROBOT DRIVES							n of drive	12		CO2					
11		systems, general ty										CO2					
		system, Hydraulic	1		-												
		control valves, Rot	ary actuators	ele	ecti	rica	al (drives, DO	C: motors,								
		stepper motor and o		isn													
III		ROBOTEND-EFI		_		Rot			-Effectors			CO3					
		Classification of					-										
		, ,	netic, vacuum and adhesive grippers, hooks, devices, active and passive Grippers.														
IV		SENSORS AND								12		CO4					
		Intelligent Robot	Intelligent Robots Artificialintelligence and automated														
		manufacturing, AI		-				_	•								
		sensorydevices, typ															
		Languages and pr															
		numerical control- unit CNCsoftware	reatures of	ı C	INC	J - (/IN	C macnin	ie control								
V		PROGRAMMAB	LE LOGIC	1 ($\overline{\gamma}$	NΊ	ΓR	OLLERS	(PLC)	12		CO5					
•		Discrete Process							, ,								
		logic diagrams-Pro	_				-		_								

	of thePLC, PLC operating cycle-Additional capabilities	of								
	PLC, Programming the PLC-Personal computers using so									
	logic. Introduction to HMI, DCS and SCADA systems									
	Total	60								
	Course Outcomes	·								
Course	On completion of this course, students will;									
Outcomes										
CO1	Explain the robotics and automation	PO1								
CO2	CO2 Differentiate the essential and non-essential automation and PO1									
	logic controls									
CO3	Assess the intelligent robots and sensors	PO4, PO5, PO6								
CO4	Discuss and evaluate the end effectors	PO4, PO5, PO6								
CO5	Appraise the PLC logics	PO5, PO6, PO9								
	Text Books									
1. Robotics tecl	nnology and flexible automation by S.R. DEB Tata Mc Graw Hill									
2. Mikell P. Gro	2. Mikell P. Groover, -Automation Production systems and Computer Integrated Manufacturing, Prentice-									
Hall India, Nev	Hall India, New Delhi, 1987. Pearson Education, New Delhi									
3. W. Bolton, -	Mechatronics, Pearson Education Asia, 2002.									
4. Introduction	to robotics, mechanics and control by John J. Craig from Addison	Wesley								
	References Books									
1 Dobatics pris	nciples and practice by Dr. K.C. Jain and Dr. L.N Agarwal from K	hanna nuhlishara								
	to robotics, mechanics and control by John J. Craig from Addison									
	oover, –Industrial Robotics-Technology, Programming and Applic	ž								
New Delhi,198		cations, wie Graw Tim,								
	. Gonzalez and C S G Lee, Robotics: Control, Sensing, Vision and	l Intelligence, McGraw								
Hill, New Delh		,								
	Web Resources									
1	https://onlinecourses.nptel.ac.in									
2	https://cac.annauniv.edu									
3 https://www.plctraininginchennai.net/										
Methods of Evaluation										
Continuous Internal Assessment Test										
Internal Assignments 25 Marks										
Evaluation Seminars										
Attendance and Class Participation										
External	End Semester Examination	75 Marks								
Evaluation										
		100 Marks								
	Methods of Assessment									

Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definitions							
Understand/	MCQ, True/False, Short essays, Concept explanations, Short summary or							
Comprehend	overview							
(K2)	OVCIVIEW							
Application	Suggest idea/concept with examples, Suggest formulae, Solve problems, Observe,							
(K3)	Explain							
Analyza (I/A)	Problem-solving questions, Finish a procedure in many steps, Differentiate							
Analyze (K4)	between various ideas, Map knowledge							
Evaluate (K5)	Longer essay/ Evaluation essay, Critique or justify with pros and cons							
Create (K6)	Check knowledge in specific or off-beat situations, Discussion, Debating or							
Create (K6)	Presentations							

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	M										
CO2	M										
CO3				S	S	S					
CO4				S	S	S					
CO5					S	S			S		

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.	Marks		
Code							dits	Hour	CI	Exter	Total
								S	A	nal	
	CONSUMER ELECTRONIC APPLIANCES	Skill enhance ment Course (SEC-1 NME)	2	-	-	-	2	2	25	75	100

Course Objectives

CO1	Describe the concepts of microwave ovens
CO2	Understand the concepts of washing machines
CO3	Know about air conditioners and refrigerators
CO4	Gain knowledge about home or office digital devices

CO5	Learn about digital access services such as LAN, MODEM, A	ГМ					
UNIT	Details	No.of Hours	Course Objectives				
I	MICROWAVE OVENS: Microwaves - Properties and generation Magnetrons, Waveguides microwave oven block diagram - LCD timer with alarm - Single chip controllers - Types of microwave ovens- microwave Cooking-Features and parts of microwave oven-Wiring and safety instructions - Microwave cookware - Operating problem and solutions- Care and cleaning		CO1				
II	WASHING MACHINES: Electronic controller for washing machines - Washing machine hardware -Washing cycle-Hardware and software development - Types of washing machines -Fuzzy logic washing machines - Features of washing machines.		CO2				
III	AIR CONDITIONERS AND REFRIGERATORS: Air Conditioning - Components of air conditioning systems - All water air conditioning systems - All air conditioning systems - Remote control buttons-Combination systems- Unitary and central air conditioning systems - Split air conditioners-Refrigeration- Refrigerants-Refrigeration Systems-Domestic Refrigerators		CO3				
IV	HOME / OFFICE DIGITAL DEVICES: Facsimile machine –Basic fax machine operations-Group 3 fax machines- Xerographic copier, Process-Extension to dynamic copier - Digital clocks - Block diagram of a digital clock-LSI digital clock.		CO4				
V	DIGITAL ACCESS SERVICES: ISDN-The Internet- LAN - Functions and networks – MODEM - Barcode- Barcode Scanner and decoder -Bluetooth and Wireless enabled devices - Electronic Fund Transfer - Automated Teller Machines (ATMs) - Set-Top boxes - Digital cable TV		CO5				
	Total	30					
	Course Outcomes						
Course Outcomes	On completion of this course, students will;						
CO1	Identify the consumer electronic application PO1, PO5, PO6						
CO2	Associate various digitally made instruments PO1, PO2, PO3, PO6, PO9						
CO3	Choose the appropriate digital services P	O1, PO5,	PO6				

CO4	A	DO4 DO5 DO6										
CO4	Acquire the knowledge of digital devices enhancement	PO4, PO5, PO6										
CO5	Recommend the usage of alternate digital resources	PO1, PO5, PO6										
Text Books												
1.	Consumer Electronics - S.P. Bali, Pearson Education, New											
2.	Consumer electronics by Deepak Arora, Eagle Prakashan, J	alandhar.										
References Boo												
1	Consumer electronics by Yagnik and Jain- Ishan Publication	n										
2	Service manuals, BPB Publication, New Delhi											
Web Resources												
1	1 https://archive.nptel.ac.in/courses											
2	https://esdm-skill.deity.gov.in											
	Methods of Evaluation											
	Continuous Internal Assessment Test											
Internal	Assignments	25 Marks										
Evaluation	Seminars	23 Warks										
	Attendance and Class Participation											
External Evaluation	End Semester Examination	75 Marks										
Evaluation	Total	100 Marks										
	Methods of Assessment	100 Warks										
	Victious of Assessment											
Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definition	S										
Understand/												
Comprehend	MCQ, True/False, Short essays, Concept explanations, sho	ort summary or overview										
(K2)												
Application	Suggest idea/concept with examples, suggest formulae,	solve problems, Observe,										
(K3)	Explain											
Analyza (IZA)	Problem-solving questions, finish a procedure in many s	teps, Differentiate between										
Analyze (K4)	various ideas, Map knowledge											
Evaluate (K5)	Longer essay/ Evaluation essay, Critique or justify with pro-	os and cons										
Create (K6)	Check knowledge in specific or offbeat situations, Presentations	Discussion, Debating or										
Manning with	Programme Outcomes:											

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	S				S	S					
CO2	S	S		M	S	S			M		

CO3			M	S	S			
CO4	S		S	S	M			
CO5	S			S	S			

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.		Ma	ırks
Code							dits	Hour	CI	Exter	Total
								S	A	nal	
	FOUNDATION OF	Skill	2	-	-	-	2	2	25	75	100
	ELECTRONICS&	enhance									
	COMPUTERS	ment									
		(Foundat									
		ion									
		Course)									
		Co	our	se C)bje	ctives					
CO1		Describe t	he o	conc	epts	of bas	ic semi	conducto	rs		
CO2		Unders	tanc	l the	con	cepts o	of diode	circuits			
CO3		Know	ab	out	trans	istors a	ınd amp	olifiers			
CO4	Ga	ain knowled	ge (of co	ompu	iter sof	tware's	and lang	uages		
CO5		Learn how	to	solv	e pro	blems	and the	eir conce	ots		
UNIT		De	tail	S					N	o.of	Course
									Н	ours	Objectives
I	SEMICONDUCTOR							conducto	r	6	CO1
	materials, intrinsic					nicondu		p-typ	e		
	semiconductors, n- typ									-	
II	DIODE CIRCUITS:									6	CO2
	wave rectifiers, center						slock d	iagram c	1		
III	DC power supply, Zen TRANSISTORS AN						'RS. P	RIT FET	7	6	CO3
111	and MOSFET transisto										CO3
	feedback, negative a										
	Hartley, Colpitts oscill	1					_	2			
IV	COMPUTER SOFTV									6	CO4
	i) Type of So:	ftware's - Sy	ste	m A	rchi	tecture					
		anguage			-		ge - H	igh Leve	el		
	<u> </u>	Object Orie									
V	PROBLEM SOLVIN									6	CO5
	Constant Variables - D										
	and Equations - Orga			utio	n: A	malysii	ng the	problem	-		
	Algorithm - Flowchart	- Pseudo co	ae								

	PROGRAMMING STRUCTURE: Modules and their function	-								
	Local and Global variables - Parameters - Return values - Sequentia	1								
	Logic Structure	20								
	Total	30								
	Course Outcomes	l								
Course	On completion of this course, students will;									
Outcomes										
CO1										
CO2	Understand and differentiate the concepts of basic	PO1, PO2	, PO3, PO5,							
	semiconductors	PO6, PO9								
CO3	Learn amplifiers and circuit theory	PO1, PO5,	PO6							
CO4		PO4, PO5,	PO6							
CO5	Recommend the usage of software's in electronic devices PO1, PO5, PO6									
	Text Books									
1.	Basic and Applied Electronics-T. K Bandyopadhyay, Books a	nd Allied I	Pvt Ltd (2002)							
2.	B.L.Theraja, "Basic solid-state Electronics", S.Chand&Co									
3.	V.K.Mehta, "Principles of Electronics", S.Chand& Co									
4.	R.L.Boylestad, L.Nashelsky, Electronic Devices and C	Circuit The	eory, Pearson							
5.	Education (2006).	1-	C:-41. E 4:4:							
3.	Pradeep K.Sinha and Priti Sinha, (2004) —Computer Fund BPB Publications	iamentais ₁ ,	Sixin Edition,							
6.	Maureen Sprankle and Jim Hubbard, (2009) —Problem S	olving and	Programming							
	Concept, Ninth Edition, Prentice Hall.	J	2 2							
	References Books									
1	N Bhargava, D C Kulshreshtha and S C Gupta, Basic Electron	nics and lin	ear circuits,							
	Tata McGraw-Hill (2007).									
2	J. Millman and C. Halkias, Integrated Electronics, Tata McGi	`	<u> </u>							
3	C. S. V. Murthy, (2009)—Fundamentals of Computers, Publishing House	Third Edit	ion, Himalaya							
	Web Resources									
1	http://www.tutorialspoint.com/computer_fundamentals/									
2	http://www.top-windows-tutorials.com/computer-basics/									
3	http://www.homeandlearn.co.uk/									
4	https://archive.nptel.ac.in/courses									
	Methods of Evaluation									
	Continuous Internal Assessment Test									
Interna	Assignments									
Evaluation		Marks								
L varuati	Attendance and Class Participation									
	1 Total data Class I at traipation									

External Evaluation	End Semester Examination	75 Marks									
	Total	100 Marks									
	Methods of Assessment										
Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definition	ns									
Understand/											
Comprehend	MCQ, True/False, Short essays, Concept explanations, short summary or overview										
(K2)											
Application	Suggest idea/concept with examples, suggest formulae	, solve problems, Observe,									
(K3)	Explain										
Analyze (K4)	Problem-solving questions, finish a procedure in many various ideas, Map knowledge	steps, Differentiate between									
Evaluate (K5)	Longer essay/ Evaluation essay, Critique or justify with p	ros and cons									
Create (K6)	Check knowledge in specific or offbeat situations, Discussion, Debating or Presentations										

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	S				S	S					
CO2	S	S		M	S	S			M		
CO3				M	S	S					
CO4	S			S	S	M					
CO5	S				S	S					

SEMESTER II

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.	Marks			
Code							dits	Hour	CI	Exter	Total	
								S	A	nal		
	DIGITAL	Core	4	-	-	-	4	5	25	75	100	
	ELECTRONICS	Course III										
		(CC-III)										
	Course Objectives											
CO1	Study the basic principles of number systems and codes											

CO3	CO2	Understand the basic concepts of digital logic families			
COS Study registers and memories. No. of Unit Details No. of Hours No. of Hours Objectives	CO3	Analyse arithmetic circuits.			
Unit Details NuMBER SYSTEM AND CODES: Decimal, Binary, Octal and Hexadecimal number systems, base conversions, representation of signed and unsigned numbers, BCD code, binary, octal and hexadecimal BCD-Excess3, Gray code-alphanumeric codes. Il DIGITAL LOGIC FAMILIES: Fan-in, Fan out, Noise Margin, Power Dissipation, Figure of merit, Speed power product, comparison of TTL and CMOS families. Truth Tables of OR, AND, NOT, NOR, NAND, EX-OR, Universal gates, Basic postulates and fundamental theorems of Boolean algebra, Demorgan's Theorem. Karnaugh Maps: two, three and four variable K-Map Ili ARITHMETIC CIRCUITS: Binary addition, Half and Full Adder. Half and Full subtractor, Binary Adder/Subtractor. Multiplexers, De-multiplexers, Decoders, Encoders. Parity checker – parity generators – code converters Iv LATCHES: Latches, Flip-flops - SR, JK, D, T, and Master-Slave - Edge triggering – Level triggering asynchronous ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Synchronous Up/Down counters – Programmable counters – Modulo–n counter V REGISTERS AND MEMORIES: Registers – shift registers – bift registers – Shift registers – Shift register shift registers – Shift registers – Shift register shift registers – Shift register ounters – Shift counters-Memory devices -classification of memories – ROM – ROM organization – PROM – EPROM – EPROM – EAPROM, RAM – RAM organization – Static RAM Cell Total 60 Course Outcomes On completion of this course, students will; On completion of this course, students will; PO6, PO9 CO2 Know the concept of logical families. PO6, PO7, PO9 CO3 Explain the methods of arithmetic circuits. PO6, PO9 PO6, PO9	CO4	Observe various Latches			
I NUMBER SYSTEM AND CODES: Decimal, Binary, Octal and Hexadecimal number systems, base conversions: representation of signed and unsigned numbers, BCD code. binary, octal and hexadecimal-BCD-Excess3, Gray code-alphanumeric codes. II DIGITAL LOGIC FAMILIES: Fan-in, Fan out, Noise Margin, Power Dissipation, Figure of merit, Speed power product, comparison of TTL and CMOS families. Truth Tables of OR, AND, NOT, NOR, NAND, EX-OR, Universal gates, Basic postulates and fundamental theorems of Boolean algebra, Demorgan's Theorem. Karnaugh Maps: two, three and four variable K-Map III ARITHMETIC CIRCUITS: Binary addition. Half and Full Adder. Half and Full subtractor, Binary Adder/Subtractor. Multiplexers, De-multiplexers, Decoders, Encoders. Parity checker – parity generators – code converters IV LATCHES: Latches, Flip-flops - SR, JK, D, T, and Master-Slave – Edge triggering – Level triggering asynchronous ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Synchronous Up/Down counters – Programmable counters – Modulo – counter V REGISTERS AND MEMORIES: Registers – shift registers – Universal shift registers – Shift register counters – Rom – ROM organization – Static RAM Cell Total Course Outcomes Course Outcomes Course On completion of this course, students will; CO2 Know the concept of logical families. PO6, PO9 CO3 Explain the methods of arithmetic circuits. PO6, PO9 CO4 Describe latches, registers and memories.	CO5	Study registers and memories.			
Hexadecimal number systems, base conversions, representation of signed and unsigned numbers, BCD code. binary, octal and hexadecimal-BCD-Excess3, Gray code-alphanumeric codes. II DIGITAL LOGIC FAMILIES: Fan-in, Fan out, Noise Margin, Power Dissipation, Figure of merit, Speed power product, comparison of TTL and CMOS families. Truth Tables of OR, AND, NOT, NOR, NAND, EX-OR, Universal gates, Basic postulates and fundamental theorems of Boolean algebra, Demorgan's Theorem. Karnaugh Maps: two, three and four variable K-Map III ARITHMETIC CIRCUITS: Binary addition. Half and Full Adder. Half and Full subtractor, Binary Adder/Subtractor. Multiplexers, De-multiplexers, Decoders, Encoders. Parity checker – parity generators – code converters IV LATCHES: Latches, Flip-flops - SR, JK, D, T, and Master-Slave - Edge triggering – Level triggering asynchronous ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Synchronous Up/Down counter - Programmable counters – Modulo – counter V REGISTERS AND MEMORIES: Registers – shift registers - Universal shift registers – Shift register counters – Ring counter – Shift counters-Memory devices - classification of memories – ROM – ROM organization – PROM – EPROM – EPROM – EAPROM, RAM – RAM organization – Static RAM Cell Total	Unit	Details			
Power Dissipation, Figure of merit, Speed power product, comparison of TTL and CMOS families. Truth Tables of OR, AND, NOT, NOR, NAND, EX-OR, Universal gates, Basic postulates and fundamental theorems of Boolean algebra, Demorgan's Theorem. Karnaugh Maps: two, three and four variable K-Map III ARITHMETIC CIRCUITS: Binary addition. Half and Full Adder. Half and Full subtractor, Binary Adder/Subtractor. Multiplexers, De-multiplexers, Decoders, Encoders. Parity checker – parity generators – code converters IV LATCHES: Latches, Flip-flops - SR, JK, D, T, and Master-Slave - Edge triggering – Level triggering asynchronous ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Modulo–n counter V REGISTERS AND MEMORIES: Registers – shift registers – Modulo–n counter Shift registers – Shift register counters – Ring counter – Shift counters-Memory devices -classification of memories – ROM – ROM organization - PROM – EPROM – EPROM – EAPROM, RAM – RAM organization – Static RAM Cell Total	I	Hexadecimal number systems, base conversions. representation of signed and unsigned numbers, BCD code. binary, octal and	12	CO1	
III ARITHMETIC CIRCUITS: Binary addition. Half and Full Adder. Half and Full subtractor, Binary Adder/Subtractor. Multiplexers, De-multiplexers, Decoders, Encoders. Parity checker – parity generators – code converters IV LATCHES: Latches, Flip-flops - SR, JK, D, T, and Master-Slave – Edge triggering – Level triggering asynchronous ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Synchronous Up/Down counters – Programmable counters – Modulo – n counter V REGISTERS AND MEMORIES: Registers – shift registers – Shift counters-Memory devices – classification of memories – ROM – ROM organization – PROM – EPROM – EEPROM – EAPROM, RAM – RAM organization – Static RAM Cell	II	Power Dissipation, Figure of merit, Speed power product, comparison of TTL and CMOS families. Truth Tables of OR, AND, NOT, NOR, NAND, EX-OR, Universal gates, Basic postulates and fundamental theorems of Boolean algebra, Demorgan's Theorem.	12	CO2	
Edge triggering – Level triggering asynchronous ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Synchronous Up/Down counters – Programmable counters – Modulo—n counter V REGISTERS AND MEMORIES: Registers – shift registers - Shift registers – Shift registers – Shift registers – Shift registers – Shift counters—Memory devices -classification of memories – ROM – ROM organization – PROM – EPROM – EPROM – EAPROM, RAM – RAM organization – Static RAM Cell Total 60 Course Outcomes Course Outcomes Course On completion of this course, students will; Outcomes CO1 Describe the outcomes of number systems. PO6, PO9 CO2 Know the concept of logical families. PO6, PO9 CO3 Explain the methods of arithmetic circuits. PO6, PO9	III	Half and Full subtractor, Binary Adder/Subtractor. Multiplexers, De-multiplexers, Decoders, Encoders. Parity checker – parity	12	CO3	
Universal shift registers – Shift register counters – Ring counter – Shift counters-Memory devices -classification of memories – ROM – ROM organization - PROM – EPROM – EAPROM, RAM – RAM organization – Static RAM Cell Total Course Outcomes Course Outcomes Columber Outcomes Columber Outcomes Columber Outcomes Columber Outcomes Columber Outcomes of number systems. Po6, Po9 Co2 Know the concept of logical families. Po6, Po7, Po9 Co3 Explain the methods of arithmetic circuits. Po6, Po9 Co4 Describe latches, registers and memories. Po6, Po9	IV	Edge triggering – Level triggering asynchronous ripple or serial counter – Asynchronous Up/Down counter - Synchronous counters – Synchronous Up/Down counters – Programmable counters –		CO4	
Course Outcomes Course Outcomes On completion of this course, students will; CO1 Describe the outcomes of number systems. PO6, PO9 CO2 Know the concept of logical families. PO6, PO7, PO9 CO3 Explain the methods of arithmetic circuits. PO6, PO9 CO4 Describe latches, registers and memories. PO6, PO9	V	V REGISTERS AND MEMORIES: Registers – shift registers – Universal shift registers – Shift register counters – Ring counter – Shift counters-Memory devices -classification of memories – ROM – ROM organization - PROM – EPROM – EEPROM – EAPROM,		CO5	
Course Outcomes Outcomes Outcomes CO1 Describe the outcomes of number systems. PO6, PO9 CO2 Know the concept of logical families. PO6, PO7, PO9 CO3 Explain the methods of arithmetic circuits. PO6, PO9 CO4 Describe latches, registers and memories. PO6, PO9			60		
OutcomesCO1Describe the outcomes of number systems.PO6, PO9CO2Know the concept of logical families.PO6, PO7, PO9CO3Explain the methods of arithmetic circuits.PO6, PO9CO4Describe latches, registers and memories.PO6, PO9	~				
CO2 Know the concept of logical families. PO6, PO7, PO9 CO3 Explain the methods of arithmetic circuits. PO6, PO9 CO4 Describe latches, registers and memories. PO6, PO9	Outcom	es			
CO3 Explain the methods of arithmetic circuits. PO6, PO9 CO4 Describe latches, registers and memories. PO6, PO9	CO1	Describe the outcomes of number systems.	PO6, PO9		
CO4 Describe latches, registers and memories. PO6, PO9	CO2	Know the concept of logical families.	PO6, PO7, PO9		
	CO3	Explain the methods of arithmetic circuits.	PO	O6, PO9	
CO5 Elaborate on the digital logic families PO6, PO9	CO4	Describe latches, registers and memories.	PO6, PO9		
	CO5	Elaborate on the digital logic families	PO6, PO9		

	Text Books								
1	Digital Principles & Applications – Albert Paul Malvino & L	each							
2	Digital Fundamentals – Thomas L. Floyd – Prentice Hall								
Digital Electronics-an introduction to Theory and Practice - William H.Goth Prentice Hall									
	References Books								
1	Digital Practice using Integrated Circuits – R. P. Jain and An	and							
2	Fundamentals of Digital Circuits, Anand Kumar, 2nd Edn, 20	009, PHI Learning Pvt. Ltd.							
3	Digital Circuits and systems, Venugopal, 2011, Tata McGrav	v Hill.							
4	Digital Systems: Principles & Applications, R.J.Tocci, N.S.W	Vidmer, 2001, PHI Learning							
5	Digital Principles, R. L. Tokheim, Schaum's Outline Series,	Tata McGraw- Hill (1994)							
	Web Resources								
1	https://onlinelibrary.wiley.com/doi/book/10.1002/978047051	0520							
2	https://www.freebookcentre.net/electronics_communication_	books/Digital-Electronics-							
	Books-Download.html								
	Methods of Evaluation								
	Continuous Internal Assessment Test								
Internal	Assignments	25 Marks							
Evaluation	Seminars	23 WILLIAS							
	Attendance and Class Participation								
External Evaluation	End Semester Examination	75 Marks							
	Total	100 Marks							
	Methods of Assessment								
Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definition	ns							
Understand	/								
Comprehen	MCQ, True/False, Short essays, Concept explanations, short summary or overview								
(K2)									
Application	Suggest idea/concept with examples, suggest formulae, solve problems, Observe,								
(K3)	Explain								
Analyze (K4	eps, Differentiate between								
Evaluate (K5)	Longer essay/ Evaluation essay, Critique or justify with pr	ros and cons							
Create (K6)	Check knowledge in specific or offbeat situations, Discussion, Debating or Presentations								

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1						M			M		
CO2						M	L		M		
CO3						M			M		
CO4						M			M		
CO5						M			M		

Subject	Subject Name	Catego ry	L	Т	P	S	Cre dits	Inst.	Marks					
Code								Hours 3	CIA 50	Exter nal 50	Total			
		CCIV-	-	-	3	-	3				100			
	DIGITAL	CORE PRAC TICAL												
	ELECTRONICS													
	LAB													
		II (CC-												
		II-P)												
			Cours		0	ives								
CO1	Understand the prin	nciples of r	notili	ty tes	st.									
	** 1 1 1					.1 1								
CO2	Understand the bas	nderstand the basic concepts of staining methods.												
CO2	T 4h - h4i-1		- 1:00	·	4	1 1 .	1	1. i	14					
CO3	Learn the bacterial	Learn the bacterial count using different methods and anaerobic culture.												
CO4	CO4 Study the morphological demonstration of microorganisms and identif					ntificatio	\n							
CO4	Study the morphore	igical delli	onsu	111011	01 11	1100	igainsi	iis and ide	mmancan)11.				
CO5	Study the biochemi	cal identif	icatio	n of	the b	acteri	<u>а</u>							
202			Cutto											
No. of		I	Detail	S					No.of	Cor	urse			
Experim	en										ctives			
ts														
1	Study and verify tr	uth tables	of A	ND,	OR,	NOT	, NAN	D, NOR						
	and XOR gates									C	O1			
2	Design all logic gat	Design all logic gates using NAND gate				CO2								
3	Design all logic gat	es using N	OR g	ate						C	Э3			
4	Verify Demorgan's	theorem								C	Э4			
5	Construction of gat	ec ucina di	corate	2 000	non	onta				C	O5			

7	Truth table verification of Half adder and Full adder								
8	Truth table verification of Half subtractor and Full subtractor								
9	Multiplexer using 74153 IC and De-Multiplexer using 74155 IC								
10	Encoder using 74147 IC and Decoder using 7442 IC								
11	Study of M-S and J-K Flip flops using 7476 IC								
12	Parallel-in and Parallel-out Shift register using 7495 IC								
13	Up counter using 7490 IC or 7493 IC								
14	Clock generation using NAND or NOR gate								
	Total	30							
	Course Outcomes								
Course	On completion of this course, students will;								
Outcomes									
CO1	Describe and verify logic gates truth tables.	PO6, PO	07, PO8, PO9,						
	7 6 6	PO11							
CO2	Demonstrate logic gates using NAND and NOR gates.		07, PO8, PO9,						
202	Demonstrate regre gates using 141142 and 14614 gates.	PO11	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,						
CO3	Construct and verify theorems.		07 POS POS						
CO3	Construct and verify theorems.	PO6, PO7, PO8, PO9,							
CO4	Donor of the Address College to Maltindana Francisco Decades	PO11	7 DO0 DO0						
CO4	Demonstrate Adder, Subtractor, Multiplexor, Encoder, Decoder								
~~-		PO11							
CO5	Describe Flip-flops, shift registers, clock generation using ICs.	PO6, PO7, PO8, PO9,							
		PO11							
	Text Books								
1	M. Morris Mano Digital System Design, Pearson Education Asia	a (FourthEc	lition)						
2	Thomas L. Flyod, Digital Fundamentals, Pearson Education Ass	ia (1994)							
	References Books								
-	W. H. Gothmann, Digital Electronics: An Introduction To The	eory And Pr	ractice, Prentice						
1	Hall of India (2000)	J	,						
2	R. L. Tokheim, Digital Principles, Schaum's Outline Series, Tat	a McGraw-	Hill (1994)						
	Web Resources		11111 (155.1)						
1	https://www.technicalbookspdf.com/electronic-engineering/digi	tal_electron	ics/						
2	https://easyengineering.net/digital-electronics-by-godse/	tai-cicciioii	ics/						
	Methods of Evaluation								
	Continuous Internal Assessment Test								
Internal	Assignments	50 N	Marks						
Evaluation	Seminars	<i>50</i> IV	IMINO						
	Attendance and Class Participation								
External		50 B	4 1						
	End Semester Examination	50 N	1arks						

	Total	100 Marks
	Methods of Assessment	
Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definition	18
Understand/		
Comprehend	MCQ, True/False, Short essays, Concept explanations, Sh	ort summary or overview
(K2)		
Application	Suggest idea/concept with examples, Suggest formulae,	Solve problems, Observe,
(K3)	Explain	
Analyze (K4)	Problem-solving questions, Finish a procedure in many s various ideas, Map knowledge	steps, Differentiate between
Evaluate (K5)	Longer essay/ Evaluation essay, Critique or justify with pr	ros and cons
Create (K6)	Check knowledge in specific or offbeat situations, Presentations.	Discussion, Debating or

	0										
	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1						M	L	M	L		M
CO2						M	M	L	M		L
CO3						L	M	M	L		M
CO4						L	M	M	M		M
CO5						M	M	M	M		M

Subject	Subject Name	Categor	L	T	P	S	Cre	Inst.		Marks	
Code		y					dits	Hou	CIA	External	Total
								rs			
	INTRODUCTION	Allied	0	3	-	-	3	2	25	75	100
	TO PYTHON	Course -									
	LANGUAGE	2 (AL-2)									
		Cour	se o	bje	ctiv	es					

CO1	Understand the basic python programming language		
CO2	Learn data structures and concepts		
CO3	Know the files, exceptions of python program		
CO4	Understand tuples, dictionaries and dictionaries		
CO5	Use and adopt GUI in python program		
UNIT	Details	No.of	Course
		Hours	Objectives
I	BASICS OF PYTHON PROGRAMMING: Features of	4	CO1
	Python, variables and identifiers, operators and expressions.		
	Decision control Statements: Selection/Conditional branching		
	statements, basic loopstructures/iterative Statements, nested		
	loops, break, continue, and pass Statements. Functions and		
	Modules: function definition, function call, more on defining functions, recursive functions, modules.		
II	DATA STRUCTURES: Strings: Introduction, built-in string	4	CO2
11	methods and functions, slice operation, StringModule. Regular	-	CO2
	Expressions. Lists: Introduction, nested list, cloning lists, basic		
	list operations, list methods. Functional programming: filter(),		
	map(),reduce() function.		
III	FILES AND EXCEPTIONS: Read and writing files, pickling,	4	CO3
	handling exceptions. Built-in and user-defined exceptions. OOPS		
	Concepts: Introduction, classes and object, class method and		
	self-argument, the init () method, class variables and object		
	variables, public and private data members, Inheritance,		
T	Operator Overloading.		GO 4
IV	TUPLES: Introduction, basic tuple operations, tuple	4	CO4
	assignment, tuples for returning multiple values, nested tuples, tuple methods and functions. Set: Introduction, Set		
	operations. Dictionaries: Basic operations, sortingitems,		
	looping over dictionary, nested dictionaries, built-in dictionary		
	functions.		
V	GRAPHICAL USER INTERFACES: Behaviour of terminal-	4	CO5
	based programs and GUI-based programs, Codingsimple GUI-		
	based programs, other useful GUI resources. GUI		
	Programming: Graphical User Interfaces, Usingthe tkinter		
	Module, Display text with Label Widgets, Organizing, Widgets		
	with Frames, Button Widgets and InfoDialog Boxes, Getting		
	Input with Entry Widget, Using Labels as Output Fields, Radio		
	Buttons, Check Buttons.	20	
	Total	20	
	Course Outcomes		
Course	On completion of this course, students can able to:		
Outcomes			

CO1	Study the concept of basic python programming language.	PO5, PO6, PO10							
CO2	Gain the knowledge of data types.	PO10							
CO3	Understand the various types of files and exceptions	PO11							
CO4	Define, Explain and Need of python program	PO4, PO11							
CO5	Understand the tuples and GUI interfaces of python	PO4, PO11							
	Text Books								
1	Kenneth A. Lambert, The Fundamentals of Python: First F	Programs, 2011, Cengage							
2	Learning. Think Python First Edition, by Allen B. Downey, Orielly published.	hina							
2	References Books	mig							
1	Reema Thareja, "Python programming using problem sol	lying approach" Oxford							
university press.									
2	Allen Downey," Think Python: How to Think Like a Comp	puter Scientist", O'Reilly							
	publications,2nd Edition								
3	Albert Lukaszewski, "My SQL for python", PACKT publishers								
4	Mark Lutz, "Learning Python", O'Reilly Publications Web Resources								
1	http://nptel.ac.in/courses/117106113/34								
2	www.scipy-lectures.org/intro/language/python_language.html								
2	Methods of Evaluation								
	Continuous Internal Assessment Test								
Internal	Assignments								
Evaluation		—— 25 Marks							
2 varaution	Attendance and Class Participation								
External	•								
Evaluation	End Semester Examination	75 Marks							
	Total	100 Marks							
	Methods of Assessment								
Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definitions								
Understand									
Comprehen	d MCQ, True/False, Short essays, Concept explanations, short	summary or overview							
(K2)									
Application		lve problems, Observe,							
(K3)	Explain	5:00							
Analyse (K4	Problem-solving questions, finish a procedure in many steps various ideas, Map knowledge	s, Differentiate between							
Evaluate (K									
Create (K6	Check knowledge in specific or offbeat situations, Di Presentations	scussion, Debating or							

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1					M	M				M	
CO2										M	M
CO3											S
CO4				M							S
CO5				M							S

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.		Ma	rks
Code							dits	Hour	CI	Exte	r Total
								S	A	nal	
	OPERATIONAL AMPLIEUDS	Elective	4	-	-	-	3	4	25	75	100
	AMPLIFIERS AND ITS	Generic									
	APPLICATIONS	/Discipline									
		Specific Elective II									
		Course		hio	otiv	06					
		Cours	e O	obje	CUV	es					
CO1	Understand the OPA	MPs and stud	y tl	he p	araı	nete	ers of I	C.			
CO2	To gain knowledge	ofOPAMP circ	cuit	t des	sign						
CO3	Understand the world	king of multiv	ibr	atoı	s us	sing	IC 55:	5 timer a	nd V-	F inter	-conversion
	using special applica	tion ICs 565 a	and	560	5						
CO4	To understand the pr	inciple of PLI	L a	nd s	igna	al co	ondition	ning circ	iits		
CO5	Study various fixed	and variable I	C r	egul	lato	ſS					
Unit	Details								No	o.of	Course
										ours	Objectives
I	_	l Amplifier						ifferentia			CO1
	amplifiers (Dual i	•							*		
	constant current b				-						
	amplifier stages with	_		el tr	ans	lato	r, blocl	k diagrar	n		
	of an operational am		_	14			, cc	4			
	Op-Amp paramete	-			_	-					
	input bias currer capacitance, offset	nt, differenti									
	range, common mod					_	_	_			
	rejection ratio.	ic rejection r	ailC	, 51	CW	rate	, suppl	y vonag			
II	Op-Amp Circuits	Onen and	C	lose	ed	loor	o conf	iguration	1, 12		CO2

		T	
	Frequency response of an op-amp in open loop and closed loop configurations, Inverting, Non-inverting, Summing and difference amplifier, Integrator, Differentiator, Voltage to current converter, Current to voltage converter. Comparators: Basic comparator, Level detector, Voltage limiters, Schmitt Trigger.		
III	Signal generators: Phase shift oscillator, Wein bridge oscillator,	12	CO3
	Square wave generator, triangle wave generator, saw tooth wave	12	CO3
	generator, and Voltage controlled oscillator (IC 566).		
	Multivibrators (IC555): Block diagram, Astable and		
	monostable multivibrator circuit, Applications of Monostable		
	and Astable multivibrators.		
IV		12	CO4
1 V	Phase locked loops (PLL): Block diagram, phase detectors,	12	CO4
	IC565. 37 Fixed and variable IC regulators: IC 78xx and IC		
V	79xx -concepts only, IC LM317- output voltage equation	12	CO5
V	Signal Conditioning circuits: Sample and hold systems, Active filters: First order low pass and high pass Butterworth filter,	12	CO3
	Second order filters, Band pass filter, Band reject filter, All pass		
	filter, Log and antilog amplifiers.	(0)	
	Total	60	
C	Course Outcomes		
Course	On completion of this course, students will;		
Outcomes	Cain Imperiod as about the basis of ODAMD	DO1 DC	M DO11
CO1	Gain knowledge about the basics of OPAMP		04, PO11 010, PO11
CO2	Exemplify the applications of OPAMP	,	
CO3	Acquire square wave using different modes of 555 timer IC		07, PO11
CO4	Design application-oriented circuits using Op-amp ICs.		08, PO11
CO5	Understand the non-ideal behaviour by parameter measurement of Op-amp	PO10, P	OH
	Text Books		
1.	R. A. Gayakwad, Op-Amps and Linear IC's, Pearson Education (2	2003)	
2.	R. F. Coughlin and F. F. Driscoll, Operational amplifiers and Lin	ear Integr	ated circuits,
	Pearson Education (2001)	_	
3	J. Millman and C.C. Halkias, Integrated Electronics, Tata McGrav	v-Hill (20	01)
	References Books		
1	A.P.Malvino, Electronic Principals,6th Edition, Tata McGraw-Hi	11,(2003)	
2	K.L.Kishore, OP-AMP and Linear Integrated Circuits, Pearson (20)	11)	
	Web Resources	•	

1	https://www.s.a.h.s.aks.dimostomy.com/datailg.nhm?ah.s.aks.1460	
1	https://www.e-booksdirectory.com/details.php?ebook=1469	
2	https://www.jameco.com/z/ISBN-0945053290-Master-Publishin	
	Optoelectronic-Circuits-and-Projects-Book-Vol-1-By-Forrest-M	lims_2112743.html
	Methods of Evaluation	
	Continuous Internal Assessment Test	
Interna	Assignments	25 Marks
Evaluati	on Seminars	23 Wars
	Attendance and Class Participation	
Externa	End Semester Examination	75 Marks
Evaluati	on End Semester Examination	13 Iviaiks
	Total	100 Marks
	Methods of Assessment	
Recall (K	(1) Simple definitions, MCQ, Recall steps, Concept definition	S
Understa	nd/ MCO True/Folge Short essays Concept evalenation	a ghart gummary ar
Compreh	MCQ, True/False, Short essays, Concept explanation	is, short summary or
(K2)	overview	
Applicati	on Suggest idea/concept with examples, suggest formulae, so	olve problems, Observe,
(K3)	Explain	
Analyze (Problem-solving questions, finish a procedure in ma between various ideas, Map knowledge	ny steps, Differentiate
Evaluat (K5)	Longer essay/ Evaluation essay, Critique or justify with pr	os and cons
Create (F	Check knowledge in specific or offbeat situations, D Presentations	iscussion, Debating or

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	L			M							S
CO2				L						M	S
CO3				L			M				S
CO4							S	S			S
CO5										M	S

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.		Ma	rks
Code							dits	Hour	CI	Exter	Total
								S	A	nal	
	ELECTRONIC	Skill	2	-	-	-	2	2	25	75	100
	TROUBLE	Enhance									
	SHOOTING	ment									
		Course -									
		SEC-2									
		(NME)									
		C	our	se O	bjec	tives					
CO1	Learn about electro	nic equinmer	nt's								
CO2	Make student under			ation	c an	d nred	caution	s of e-tro	nuble	shooting	
CO ₂	Learn trouble shoot		-	at I U I I	o all	a pro	caanon	.5 51 0-110	, 4010	mooning	
CO4	Impart knowledge			one	nts						
CO5	Learn knowledge of	_	-			troub	leshoot	ing tech	าเดเษต	<u> </u>	
Unit	Learn knowledge of		etai		,itai	Houb	10311001	ing teem	Inque	No.of	Course
		D	ctai	13						Hours	Objectives
I	RELIABILITY O	F ELECTRO	ONI	CE	ΩIII	PME	ENTS			5	CO1
1	Failures-Causes of				_			of pro	per	J	
		Maintenancel					_	aintenan	-		
		ntenance-Bas	sic	P	roce	dure	of	Serv	ice		
11	andMaintenance	AND DDEC	7 A T	TOT C	NIC						G02
II	PREPARATIONS Troubleshooting					n Fai	ılt Do	nair Dar	air	5	CO2
	Verification-Perform										
	Service Manual-Te				-			_			
	Cathode Ray Oscill										
III	TROUBLESHOO			-						5	CO3
	Functional Area A										
	Technique-Output Convergent paths										
	Paths Technique, M	-				15 160	Jiiiique	-Switch	mg		
IV	TESTING OF PAS									5	CO4
	Resistors, Preset,						rs, Tr	ansforme	ers,		
	Passive componer	_	_								
	Devices-Diode,	Zener		de,		L.E.I	,	Transist	,		
17	MOSFET, Thyriston									<i>E</i>	COF
V	TROUBLE SHOO Summary of Gates								ital	5	CO5
	Test Instruments-L		_								
	Tester, Faults in	_	_			_		_			
	Troubleshooting-Tr	oubleshootin	ıg-P	owe	[su	pply,	SM	PS,		

	Oscilloscope	
	Total	25
	Course Outcomes	
Course	On completion of this course, students will;	
Outcomes		
CO1	Learn the importance of analog and digital electronic troubleshooting	PO5, PO6, PO7,
		PO8, PO10
CO2	Study the precautions and preparations of trouble shooting	PO5, PO6, PO7,
		PO8, PO10
CO3	Know the methods of trouble shooting	PO5, PO6, PO7,
		PO8, PO10
CO4	Learn the importance and necessary of testing components	PO5, PO6, PO7,
		PO10
CO5	Create awareness and motivation of starting a new career to service	PO5, PO6, PO7,
	industries	PO10

		Text Books								
1.	1. Maintenance of Electronic Equipment's-K.Sudeep Singh - Kataria and Sons									
		References Books								
1.	Ele	tronic troubleshooting 4th Edition by Daniel and Aram								
		Web Resources								
1		ps://www.sapnaonline.com/books/troubleshooting-electronic-equipoair-rs-khandpur-0070483574-9780070483576	oment-includes-							
2	htt	ps://www.allaboutcircuits.com/textbook/								
		Methods of Evaluation								
_		ontinuous Internal Assessment Test	25 Marks							
Internal		ssignments								
Evaluation		minars								
		tendance and Class Participation								
External		d Semester Examination 75 Marks								
Evaluation										
	То		100 Marks							
		Methods of Assessment								
Recall (KI)		Simple definitions, MCQ, Recall steps, Concept definitions								
Understand Compreher (K2)		MCQ, True/False, Short essays, Concept explanations, Short sum	nmary or overview							
Application (K3)	1	Suggest idea/concept with examples, Suggest formulae, Solve problems, Observe, Explain								
Analyse (K	4)	Problem-solving questions, Finish a procedure in many steps, Differentiate between various ideas, Map knowledge								
Evaluate (F	K 5)	Longer essay/ Evaluation essay, Critique or justify with pros and cons								
Create (K6)	Check knowledge in specific or offbeat situations, Discuss Presentations	ion, Debating or							

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1					S	M	M	M		S	
CO2					S	M	M	M		S	
CO3					S	M	M	M		S	
CO4					S	S	L			S	
CO5					S	S	M			S	

Subject	Subject Name	Category	L	T	P	S	Cre	Inst.	Marks		
Code							dits	Hour	CI	Exter	Total
								S	A	nal	
	COMPUTER	Skill	2	-	-	-	2	2	25	75	100
	HARDWARE	Enhanceme									
		nt Course -									
		SEC-3									

	Course Objectives							
CO1	Acquire knowledge on the concepts of computer hardware.							
CO2	Describe the concept of memories, CPU and peripherals.							
CO3	Discuss about power management in PC hardware system							
CO4	Demonstrate PC drives and understand with latest device configurations.							
CO5	Understand the latest hardware usage and architecture.							
Unit	Details	No.of	Course					
		Hours	Objectives					
I	CPU: CPU essentials – processor modes – modern CPU concepts – Architectural performance features – the Intel's CPU	5	CO1					
II	MEMORY CONCEPT: Essential memory concepts – memory organizations – memory packages –modules – logical memory organizations – memory considerations – memory types – memory techniques – selecting and installing memory	5	CO2					
III	MOTHERBOARD: Active motherboards – sockets and slots – Intel D850GB – Pentium4 mother board – expansion slots – form factor – upgrading a mother board – chipsets –north bridge – south bridge	5	CO3					
IV	POWER SUPPLY: Power supplies and power management – concepts of switching regulation – potential Power problems – power management. The floppy drive – magnetic storage – magnetic recording principles – data and disk organization – floppy drive – hard drive – data organization and hard drive – sector layout	5	CO4					
V	DRIVES: IDE drive standard and features – Hard drive electronics – CDROM drive construction – CDROM electronics – DVD-ROM – DVD media – DVD drive and decoder.	5	CO5					
	Total	25						
	Course Outcomes							
Course	On completion of this course, students will;							
Outcomes								
CO1	Discuss the overall aspects of PC Hardware system	PO1, PO5, PO7						
CO2	Familiarize with the recent technologies of computer drives.	PO1, PO2						
CO3	Explain the hardware system and understand with the latest device practices.	PO1, PO5						
CO4	Understand very well about the computer motherboard architectures and peripherals.	PO7, PO8, PO10						
CO5	Understand the essentials of computer hardware's	PO5, PO	D7, PO8					

Text Books										
1	Stephen J. Bigelow, —Trouble Shooting, maintaining and Repairing PCsl, Tata McGraw-Hill, New Delhi, 2001.									
2	Craig Zacker & John Rourke, —The complete reference: PC hardware, Tata Mc Graw-Hill, New Delhi, 2001.									
	References Books									
	Telefonets Books									
1	1 Mike Meyers, Introduction to PC Hardware and Troubleshooting, Tata									
	McGraw-Hill, New Delhi,2003									
2	B.Govindarajulu, IBMPC and Clones hardware trouble shoo	oting andMaintenance, Tata								
	McGraw-Hill, New Delhi,2002 Web Resources									
	,, 62 2165641665									
1	https://egyankosh.ac.in/bitstream/123456789/33613/1/Unit-13	3.pdf								
2	https://cdn.ttgtmedia.com/searchSystemsChannel/downloads/	Windows7Bible.pdf								
	Methods of Evaluation									
	Continuous Internal Assessment Test									
Internal	Assignments	25 Marks								
Evaluation	Seminars									
	Attendance and Class Participation									
External	End Semester Examination	75 Marks								
Evaluation	Total	100 Marks								
	Methods of Assessment	100 Marks								
Recall (K1)	Simple definitions, MCQ, Recall steps, Concept definition	20								
Understand										
Compreheno		ort summary or overview								
(K2)		,								
Application	Suggest idea/concept with examples, Suggest formulae, S	Solve problems, Observe,								
(K3)	Explain									
Analyze (K4	Problem-solving questions, Finish a procedure in m between various ideas, Map knowledge	any steps, Differentiate								
Evaluate (K5)	Evaluate Longer essay/ Evaluation essay Critique or justify with pros and cons									
Create (K6)	Create (K6) Check knowledge in specific or offbeat situations, Discussion, Debating or Presentations									

	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11
CO1	S				S		S				
CO2	M				S						
CO3	S				S						
CO4							S	S		S	
CO5					S		S	S			